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Applicants: Jang-Kun SONG, et al. Application No.

In the Claims:

Please cancel claims 1-22 and 29-34 without any disclaimer or a prejudice. Listings of the pending claims are as follows:

1-22 (Cancelled)

23. (Original) A method of fabricating a liquid crystal display comprising the steps of:

forming a gate line assembly on a first substrate, the gate line assembly comprising gate lines and gate electrodes;

depositing a gate insulating layer onto the first substrate with the gate line assembly;

forming a semiconductor pattern on the gate insulating layer such that the semiconductor pattern is overlapped with the gate electrodes;

forming a data line assembly on the structured first substrate, the data line assembly comprising source and drain electrodes overlapped with side edges of the semiconductor pattern, and data lines connected to the source electrode while crossing the gate lines to thereby define pixel areas;

depositing a protective layer onto the data line assembly, the semiconductor pattern and the gate insulating layer;

forming a first protective pattern and a first gate insulating pattern on the data line assembly, the semiconductor pattern and the gate line assembly except some portion of the drain

electrode by through etching the protective layer and the gate insulating layer, and forming a protrusion pattern at the pixel area, the protrusion pattern being formed with the second protective pattern and the second gate insulating pattern;

depositing a first transparent conductive layer onto the structured first substrate; and forming a pixel electrode at the pixel area while forming an opening pattern within the pixel electrode by through etching the first transparent conductive layer such that the pixel electrode covers the protrusion pattern while contacting the drain electrode.

24. (Original) The method of claim 23 further comprising the steps of: forming color filters at a second substrate;

forming a common electrode on the color filters through depositing a second transparent conductive layer onto the substrate with the color filters; and

aligning the first and second substrates such that the pixel electrode faces the common electrode.

- 25. (Original) The method of claim 24 further comprising: coating a first vertical alignment film onto the pixel electrode; coating a second vertical alignment film onto the common electrode; and injecting a liquid crystal into the gap between the first and second substrates.
- 26. (Original) The method of claim 25 wherein the liquid crystal has a property of negative dielectric anisotropy.

- 27. (Original) The method of claim 24 wherein the second transparent conductive layer is formed with indium tin oxide or indium zinc oxide.
- 28. (Original) The method of claim 27 wherein the first transparent conductive layer is formed with indium tin oxide or indium zinc oxide.

29-34 (Cancelled)